

American National Standard

INCITS 465-2010

Reaffirmed as
INCITS 465-2010 (R2020)

*for Information Technology –
SCSI/ATA Translation - 2
(SAT-2)*

Developed by



Where IT all begins



This is a preview of "INCITS 465-2010 (R20...". Click here to purchase the full version from the ANSI store.

INCITS 465-2010

American National Standard
for Information Technology –

**SCSI/ATA Translation - 2
(SAT-2)**

Secretariat

Information Technology Industry Council

Approved November 12, 2010

American National Standards Institute, Inc.

Abstract

This standard specifies a translation layer between SCSI and ATA protocols. This translation layer is used by storage controllers to emulate objects in a SCSI logical unit using an ATA device, providing capabilities defined by SCSI standards (e.g., the SCSI Block Commands (SBC-3) and SCSI Primary Commands (SPC-4) standards). For the purposes of this standard, ATA device capabilities are defined by ATA8-AAM, ATA8-ACS, ATA8-APT, ATA8-AST, and SATA-2.6.

American National Standard

Approval of an American National Standard requires review by ANSI that the requirements for due process, consensus, and other criteria for approval have been met by the standards developer.

Consensus is established when, in the judgement of the ANSI Board of Standards Review, substantial agreement has been reached by directly and materially affected interests. Substantial agreement means much more than a simple majority, but not necessarily unanimity. Consensus requires that all views and objections be considered, and that a concerted effort be made towards their resolution.

The use of American National Standards is completely voluntary; their existence does not in any respect preclude anyone, whether he has approved the standards or not, from manufacturing, marketing, purchasing, or using products, processes, or procedures not conforming to the standards.

The American National Standards Institute does not develop standards and will in no circumstances give an interpretation of any American National Standard. Moreover, no person shall have the right or authority to issue an interpretation of an American National Standard in the name of the American National Standards Institute. Requests for interpretations should be addressed to the secretariat or sponsor whose name appears on the title page of this standard.

CAUTION NOTICE: This American National Standard may be revised or withdrawn at any time. The procedures of the American National Standards Institute require that action be taken periodically to reaffirm, revise, or withdraw this standard. Purchasers of American National Standards may receive current information on all standards by calling or writing the American National Standards Institute.

CAUTION: The developers of this standard have requested that holders of patents that may be required for the implementation of the standard disclose such patents to the publisher. However, neither the developers nor the publisher have undertaken a patent search in order to identify which, if any, patents may apply to this standard. As of the date of publication of this standard, following calls for the identification of patents that may be required for the implementation of the standard, notice of one or more such claims has been received. By publication of this standard, no position is taken with respect to the validity of this claim or of any rights in connection therewith. The known patent holder(s) has (have), however, filed a statement of willingness to grant a license under these rights on reasonable and nondiscriminatory terms and conditions to applicants desiring to obtain such a license. Details may be obtained from the publisher. No further patent search is conducted by the developer or publisher in respect to any standard it processes. No representation is made or implied that this is the only license that may be required to avoid infringement in the use of this standard.

Published by

**American National Standards Institute, Inc.
25 West 43rd Street, New York, NY 10036**

Copyright © 2010 by Information Technology Industry Council (ITI)
All rights reserved.

No part of this publication may be reproduced in any form, in an electronic retrieval system or otherwise, without prior written permission of ITI, 1101 K Street NW, Suite 610 Washington, DC 20005.

Printed in the United States of America

Contents

	Page
Foreword.....	x
Introduction	xiv
1 Scope	1
2 Normative References	4
2.1 Normative references	4
2.2 Approved references	4
2.3 References under development	4
2.4 Other references	4
3 Definitions, symbols, abbreviations, and conventions	5
3.1 Definitions	5
3.2 Symbols and abbreviations	10
3.3 Keywords	11
3.4 SAT specific terminology	11
3.5 Conventions	11
3.5.1 Overview	11
3.5.2 Numeric conventions	12
3.5.3 Bit and byte ordering	12
3.5.4 Notation for byte encoded character strings	14
3.5.5 Notation for command descriptions	14
3.5.5.1 Description	14
3.5.6 Use of field names defined in ATA standards and specifications	15
4 General	16
5 SCSI architecture	17
5.1 Overview	17
5.2 Multi-Initiator Configurations	18
5.3 Unit attention condition	19
5.4 Handling errors in ATA commands	19
5.5 ATA nexus loss	19
5.6 ATA hardware and software reset processing	20
5.7 Translation of Large Physical Sectors	20
6 Command management model	25
6.1 Overview	25
6.2 Multiple command processing	26
6.2.1 Comparison of SCSI task set management and ATA queuing	26
6.2.2 Command mapping overview	27
6.2.3 Mapping of SCSI commands to ATA queued commands	27
6.2.4 Commands the SATL queues internally	27
6.2.5 Command queuing with multiple I_T nexuses	27
6.2.6 Collateral abort with queued commands	28
6.3 Command priority	28
6.4 Task management functions	29
6.4.1 Task management functions overview	29
6.4.2 Aborting ATA queued commands	29
6.4.3 Aborting ATA non-queued commands	29
6.4.4 ABORT TASK	29
6.4.5 ABORT TASK SET	30
6.4.6 CLEAR ACA	30

6.4.7 CLEAR TASK SET	30
6.4.8 I_T NEXUS RESET	31
6.4.9 LOGICAL UNIT RESET	31
6.4.10 QUERY TASK	32
6.4.11 QUERY TASK SET	32
6.4.12 QUERY ASYNCHRONOUS EVENT	32
6.4.13 Reset task management functions	33
6.5 CONTROL Byte	33
6.6 I_T nexus loss	33
 7 Summary of SCSI / ATA command mappings	34
 8 SCSI Primary Commands (SPC) command mapping	36
8.1 INQUIRY command	36
8.1.1 INQUIRY command overview	36
8.1.2 Standard INQUIRY data	37
8.2 LOG SENSE command	39
8.2.1 LOG SENSE command overview	39
8.2.2 PC (page control) field	40
8.2.3 PAGE CODE and SUB PAGE CODE fields	40
8.3 LOG SELECT command	41
8.3.1 LOG SELECT command overview	41
8.3.2 Page control field translations	41
8.3.3 PAGE CODE field and SUBPAGE CODE field translations	41
8.4 MODE SELECT (6) command	41
8.4.1 MODE SELECT (6) command overview	41
8.4.2 MODE SELECT (6) CDB fields	42
8.5 MODE SELECT (10) command	42
8.6 MODE SENSE (6) command	42
8.6.1 MODE SENSE (6) command overview	42
8.6.2 MODE SENSE (6) CDB fields	43
8.7 MODE SENSE (10) command	43
8.8 READ BUFFER command	44
8.8.1 READ BUFFER command overview	44
8.8.2 MODE field	44
8.8.2.1 MODE field overview	44
8.8.2.2 Data mode	44
8.8.2.3 Descriptor mode	45
8.9 REQUEST SENSE command	45
8.9.1 REQUEST SENSE command overview	45
8.9.2 Format operation in progress	46
8.9.3 SMART threshold exceeded condition	46
8.9.4 Stopped power condition	46
8.9.5 Unit attention condition established	47
8.9.6 Idle power condition	47
8.9.7 Standby power condition	47
8.10 SECURITY PROTOCOL IN command	47
8.10.1 SECURITY PROTOCOL field	47
8.10.2 SECURITY PROTOCOL SPECIFIC field	47
8.10.3 ALLOCATION LENGTH field	48
8.11 SECURITY PROTOCOL OUT command	48
8.11.1 SECURITY PROTOCOL field	48
8.11.2 SECURITY PROTOCOL SPECIFIC field	48
8.11.3 TRANSFER LENGTH field	49
8.12 SEND DIAGNOSTIC command	50
8.12.1 SEND DIAGNOSTIC command overview	50
8.12.2 SELF-TEST CODE field	51

8.12.3 SELFTEST bit	52
8.13 TEST UNIT READY command	54
8.13.1 TEST UNIT READY command overview	54
8.13.2 TEST UNIT READY command translation	54
8.14 WRITE BUFFER command	55
8.14.1 WRITE BUFFER command overview	55
8.14.2 MODE field	55
8.14.2.1 MODE field overview	55
8.14.2.2 Write data mode	56
8.14.2.3 Download microcode mode 05h	56
8.14.2.4 Download microcode mode 07h	56
9 SCSI Block Commands (SBC) command mapping	58
9.1 Translating LBA and transfer length and ATA command use constraints	58
9.1.1 Overview	58
9.1.2 Direct logical block mapping model	58
9.1.3 Indirect logical block mapping model	58
9.1.4 Selection of ATA block commands	58
9.2 FORMAT UNIT command	60
9.2.1 FORMAT UNIT command overview	60
9.2.2 FORMAT UNIT parameter list	61
9.2.3 SATL defect list header field combinations	61
9.2.4 FOV bit	61
9.2.5 DCRT bit	61
9.2.6 IP bit	62
9.3 READ commands overview	62
9.3.1 READ commands operation code translation	62
9.3.2 READ commands with FUA	62
9.4 READ (6) command	63
9.5 READ (10) command	63
9.6 READ (12) command	64
9.7 READ (16) command	64
9.8 READ CAPACITY (10) command	65
9.8.1 READ CAPACITY (10) command overview	65
9.8.2 READ CAPACITY (10) parameter data	65
9.9 READ CAPACITY (16) command	66
9.9.1 READ CAPACITY (16) command overview	66
9.9.2 READ CAPACITY (16) parameter data	66
9.10 REASSIGN BLOCKS command	67
9.10.1 REASSIGN BLOCKS command overview	67
9.10.2 REASSIGN BLOCKS operation code	67
9.11 START STOP UNIT command	69
9.11.1 START STOP UNIT command overview	69
9.11.2 Processing ending status if an error occurs	76
9.11.3 START STOP UNIT START bit LOEJ bit combinations	76
9.11.4 NO_FLUSH translation	77
9.12 SYNCHRONIZE CACHE (10) command	77
9.13 SYNCHRONIZE CACHE (16) command	78
9.14 VERIFY (10) command	78
9.15 VERIFY (12) command	79
9.16 VERIFY (16) command	79
9.17 WRITE commands overview	80
9.17.1 WRITE commands operation code translation	80
9.17.2 WRITE commands with FUA	80
9.18 WRITE (6) command	81
9.19 WRITE (10) command	82
9.20 WRITE (12) command	83

9.21 WRITE (16) command	84
9.22 WRITE AND VERIFY commands overview	84
9.23 WRITE AND VERIFY (10) command	85
9.24 WRITE AND VERIFY (12) command	86
9.25 WRITE AND VERIFY (16) command	87
9.26 WRITE LONG (10) command	87
9.27 WRITE LONG (16) command	89
9.28 WRITE SAME (10) command	89
9.28.1 WRITE SAME (10) command overview	89
9.28.2 LBADATA bit and PBDATA bit	91
9.29 WRITE SAME (16) command	91
 10 Parameters for SAT implementations	93
10.1 Mode parameters	93
10.1.1 General information	93
10.1.2 Commonly used SCSI mode pages overview	93
10.1.3 Mode parameter headers	94
10.1.4 Mode parameter block descriptor fields	95
10.1.5 Control mode page	95
10.1.5.1 General translation	95
10.1.5.2 Extended self-test completion time	96
10.1.6 Read-Write Error Recovery mode page	97
10.1.7 Caching mode page	98
10.1.8 Informational Exceptions Control mode page	100
10.1.8.1 Informational Exceptions Control mode page overview	100
10.1.8.2 Method of reporting informational exceptions (MRIE)	100
10.1.9 Power Condition mode pages	100
10.1.9.1 Power Condition mode pages overview	100
10.1.9.2 ATA Power Condition mode page	101
10.1.9.3 Power Condition mode page	101
10.2 Log parameters	103
10.2.1 Log parameters overview	103
10.2.2 Application Client log page	104
10.2.2.2 LOG SELECT translation	104
10.2.2.3 LOG SENSE translation	105
10.2.3 Supported Log Pages log page	106
10.2.4 Support Log Pages and Subpages log page	106
10.2.5 Self-Test Results log page	106
10.2.5.1 Self-Test Results log page overview	106
10.2.5.2 A method of determining ATA command selection for field translations	110
10.2.5.3 Sense key and additional sense code	111
10.2.6 Informational Exceptions log page	112
10.2.6.1 Informational Exceptions log page overview	112
10.2.6.2 Additional sense code and additional sense code qualifier translations	112
10.2.6.3 Most recent temperature reading translation	113
10.3 Vital product data parameters	113
10.3.1 Vital product data parameters overview	113
10.3.2 Supported VPD pages VPD page	114
10.3.3 Unit Serial Number VPD page	114
10.3.4 Device Identification VPD page	115
10.3.4.1 Device Identification VPD page overview	115
10.3.4.2 Logical unit name	115
10.3.4.2.1 Logical unit name overview	115
10.3.4.2.2 Logical unit name derived from the world wide name	115
10.3.4.2.3 Logical unit name derived from the model number and serial number	116
10.3.4.3 Examples of additional designation descriptors	117
10.3.4.3.1 Designation descriptors included by a SATL in an ATA host	117

10.3.4.3.2 Designation descriptors included by a SATL in a SAS initiator device	118
10.3.4.3.3 Designation descriptors included by a SATL in a SCSI to ATA protocol bridge	119
10.3.5 Mode Page Policy VPD page	119
10.3.6 Block Device Characteristics VPD Page	120
11 Translation of ATA errors to SCSI errors	121
12 SAT-specific SCSI extensions	122
12.1 SAT-specific SCSI extensions overview	122
12.2 ATA PASS-THROUGH commands	122
12.2.1 ATA PASS-THROUGH commands overview	122
12.2.2 ATA PASS-THROUGH (12) command	123
12.2.3 ATA PASS-THROUGH (16) command	126
12.2.4 Mapping of ATA PASS-THROUGH CDB field translations	127
12.2.5 ATA PASS-THROUGH status return	128
12.2.6 ATA Status Return descriptor	129
12.2.7 Fixed format sense data	130
12.3 SAT-specific mode pages	132
12.3.1 SAT-specific mode pages overview	132
12.3.2 PATA Control mode page	132
12.3.3 ATA Power Condition mode page	136
12.4 SAT-specific VPD pages	137
12.4.1 SAT-specific VPD pages overview	137
12.4.2 ATA Information VPD page	137
12.4.2.1 ATA Information VPD page overview	137
12.4.2.2 ATA device signature	139
12.4.2.3 ATA IDENTIFY DEVICE data or ATA IDENTIFY PACKET DEVICE data	140
12.5 SAT-specific security protocol parameters	140
12.5.1 ATA Device Server Password security protocol	140
12.5.1.1 SECURITY PROTOCOL IN command	140
12.5.1.1.1 SECURITY PROTOCOL IN command overview	140
12.5.1.1.2 SECURITY PROTOCOL IN parameter data	141
12.5.1.1.3 SCSI commands allowed in the presence of various security modes	142
12.5.1.2 SECURITY PROTOCOL OUT command	144
12.5.1.2.1 SECURITY PROTOCOL OUT command overview	144
12.5.1.2.2 Set password parameter list	146
12.5.1.2.3 Unlock parameter list	147
12.5.1.2.4 Erase unit parameter list	147
12.5.1.2.5 Disable password parameter list	148
12.6 SAT-specific log pages	148
12.6.1 SAT-specific log pages overview	148
12.6.2 ATA PASS-THROUGH Results log page	148
Annex A SCSI to ATAPI command transmission.....	149
A.1 Introduction	149
A.2 ATAPI device model	149
A.3 SCSI CDB transmission	149
A.4 ATAPI Command management	150
A.5 SATL ATAPI implementations	150
A.6 ATAPI I_T nexus loss handling	150

Tables

	Page
1 Numbering Conventions	12
2 Example of ordering of bits and bytes within a multi-byte element	13
3 Example of ordering of bits and bytes within a multiple element	14
4 Format for translated command field descriptions	15
5 Large Physical Block Geometry Parameters	20
6 Comparison of SCSI task set management and ATA queuing methods	26
7 SATL processing of ATA commands aborted by ATA collateral abort	28
8 Command Priority to NCQ PRIO Mapping	28
9 CONTROL byte fields	33
10 Summary of SCSI / ATA Command Mapping	34
11 INQUIRY CDB field translations	36
12 Standard INQUIRY data fields	37
13 LOG SENSE CDB field translations	39
14 PC field	40
15 PAGE CODE / SUB PAGE CODE fields	40
16 LOG SELECT CDB field translations	41
17 Page control field translation	41
18 LOG SELECT PAGE CODE field and SUBPAGE CODE field translations	41
19 MODE SELECT (6) CDB field translations	42
20 MODE SELECT (10) CDB field translations	42
21 MODE SENSE (6) CDB field translations	43
22 MODE SENSE (10) CDB field translations	43
23 READ BUFFER CDB field translations	44
24 MODE field	44
25 Special Request Sense behavior reference	45
26 REQUEST SENSE CDB field translations	46
27 SECURITY PROTOCOL IN CDB field translation	47
28 SECURITY PROTOCOL OUT CDB field translation	48
29 SEND DIAGNOSTIC CDB field translations	50
30 SELF-TEST CODE field translation	51
31 SELFTTEST bit	53
32 TEST UNIT READY CDB field translations	54
33 WRITE BUFFER CDB field translations	55
34 MODE field	55
35 Download Microcode Mode 07h ATA Field Values	57
36 ATA commands used for SCSI block command translations	59
37 FORMAT UNIT CDB field translations	60
38 SATL defect list header	61
39 SATL defect list header field combinations	61
40 READ (6) CDB field translations	63
41 READ (10) CDB field translations	63
42 READ (12) CDB field translations	64
43 READ (16) CDB field translations	64
44 READ CAPACITY (10) CDB field translations	65
45 READ CAPACITY (10) parameter data	65
46 READ CAPACITY(16) CDB field translations	66
47 READ CAPACITY (16) parameter data	66
48 REASSIGN BLOCKS CDB field translations	67
49 START/STOP UNIT CDB field translations	69
50 POWER CONDITION field translation	71
51 Definition of START and LOEJ bits in the START STOP UNIT CDB	76
52 SYNCHRONIZE CACHE (10) CDB field translations	77
53 SYNCHRONIZE CACHE (16) CDB field translations	78
54 VERIFY (10) CDB field translations	78
55 VERIFY (12) CDB field translations	79

56 VERIFY (16) CDB field translations	79
57 WRITE (6) CDB field translations	81
58 WRITE (10) CDB field translations	82
59 WRITE (12) CDB field translations	83
60 WRITE (16) CDB field translations	84
61 WRITE AND VERIFY (10) CDB field translations	85
62 WRITE AND VERIFY (12) CDB field translations	86
63 WRITE AND VERIFY (16) CDB field translations	87
64 WRITE LONG (10) CDB field translations	88
65 WR_UNCOR bit and PBLOCK bit	88
66 WRITE LONG (16) CDB field translations	89
67 WRITE SAME (10) CDB field translations	90
68 LBDATA and PBDATA fields	91
69 WRITE SAME (16) CDB field translations	91
70 Summary of SCSI / ATA mode page mapping	93
71 Mode parameter header (6) fields	94
72 Mode parameter header (10) fields	94
73 Mode parameter block descriptor fields	95
74 Control mode page fields	96
75 Read-Write Error Recovery mode page fields	97
76 Caching mode page fields	98
77 Informational Exceptions Control mode page fields	100
78 Power Condition mode page fields	102
80 MODE SELECT standby timer field translation	103
81 Summary of SCSI / ATA log page mapping	103
79 MODE SENSE standby timer field translation	103
82 General usage application client parameter data fields	104
83 Parameter Storage Location	105
84 Supported Log Pages log page fields	106
85 Supported Log Pages and Subpages log page fields	106
86 Self-Test Results log page fields	106
87 Self-Test Results log parameters	107
88 ATA Self-test execution status values translated to SCSI sense keys and sense codes	111
89 Informational Exceptions log page header fields	112
90 Informational Exceptions general parameter data	112
91 ATA SMART RETURN STATUS translations	113
92 Summary of SCSI / ATA VPD page mapping	113
93 Supported VPD pages VPD page fields	114
94 Unit Serial Number VPD page fields	114
95 PRODUCT SERIAL NUMBER field	114
96 Device Identification VPD page fields	115
97 Logical unit name derived from the world wide name	115
98 Fields in the logical unit name	116
99 Logical unit name derived from the world wide name	116
100 VENDOR SPECIFIC IDENTIFIER field for logical unit name	117
101 Target port identifier for SAS	118
102 Mode Page Policy VPD page fields	119
103 Mode policy descriptor for SAT	120
104 Block Device Characteristics VPD Page field translations	120
105 Translation of ATA errors to SCSI errors	121
106 ATA PASS-THROUGH (12) command	123
107 PROTOCOL field	123
108 ATA PASS-THROUGH (12) command and ATA PASS-THROUGH (16) command DEVICE field	125
109 T_LENGTH field	125
110 ATA PASS-THROUGH (16) command	126
111 Mapping of ATA PASS-THROUGH (16) CDB fields to ATA command fields	127
112 EXTEND bit and T_LENGTH field	127

113 ATA command results	128
114 ATA Return descriptor	129
115 Fixed format sense data fields for the ATA PASS-THROUGH commands	130
116 Fixed format sense data INFORMATION field for the ATA PASS-THROUGH commands	130
117 Fixed format sense data COMMAND-SPECIFIC INFORMATION field for the ATA PASS-THROUGH commands	131
118 SCSI / ATA Translation specific mode pages	132
119 PATA Control mode page	132
120 PIO modes	133
121 MWDMA modes reported by MODE SENSE	134
122 UDMA bits requirements for changeable MODE SENSE parameters	135
123 UDMA for current MODE SENSE settings	135
124 ATA Power Condition mode page	136
125 ATA Information VPD page	137
126 ATA device signature	139
127 TRANSPORT IDENTIFIER field	139
128 ATA IDENTIFY DEVICE data or ATA IDENTIFY PACKET DEVICE data	140
129 SECURITY PROTOCOL IN parameter data	141
130 SPC commands allowed in the presence of various ATA security modes	142
131 SBC commands allowed in the presence of various ATA security modes	145
132 SECURITY PROTOCOL SPECIFIC field	146
133 Set password parameter list	146
134 Unlock parameter list	147
135 Erase unit parameter list	147
136 Disable password parameter list	148
137 SCSI / ATA Translation specific log pages	148

Figures

	Page
1 SCSI document relationships	2
2 ATA document structure	2
3 SCSI / ATA Translation document role	3
4 Example of a SATL between a SCSI application client and an ATA or ATAPI device	17
5 SATL contained within a SCSI to ATA protocol bridge	18
6 SATL contained within an ATA host	18
7 SATL contained in a SAS initiator device	18
8 Logical Sector Alignment Examples (part 1 of 3)	22
9 Logical Sector Alignment Examples (part 2 of 3)	23
10 Logical Sector Alignment Examples (Part 3 of 3)	24
11 REASSIGN BLOCKS command translation	69
12 Designation descriptors included by a SATL in an ATA host	117
13 Designation descriptors included by a SATL in a SAS initiator device	118
14 Designation descriptors included by a SATL in a SCSI to ATA protocol bridge	119

Foreword (This foreword is not part of American National Standard INCITS 465-2010.)

This standard provides a common set of definitions and requirements to establish common behavior among implementations that emulate SCSI device behavior through the combined use of ATA devices and a SCSI / ATA Translation layer (SATL). The SATL may reside in a host-based software or firmware, or it may reside in a separate component (e.g., a host bus adapter or external controller) with a separate processing unit to perform the translation. A SATL and ATA device combination may provide a functional subset of common SCSI capabilities. There is also a range of optional emulated SCSI capabilities that may be supported, depending on the capabilities of the SATL.

This standard defines SATL capabilities in terms of SCSI capabilities as defined by the applicable SCSI standards and working drafts, and defines the elements and use of ATA protocol to provide those SCSI capabilities and services in a consistent manner among SAT implementations that implement according to this standard.

Requests for interpretation, suggestions for improvement and addenda, or defect reports are welcome. They should be sent to the INCITS Secretariat, National Committee for Information Technology Standards, Information Technology Institute, 1101 K Street, NW, Suite 610, Washington, DC 20005.

This standard was processed and approved for submittal to ANSI by the International Committee for Information Technology Standards (INCITS). Committee approval of the standard does not necessarily imply that all committee members voted for approval. At the time it approved this standard, the INCITS committee had the following members:

Don Wright, Chair
Jennifer Garner, Secretary

<i>Organization Represented</i>	<i>Name of Representative</i>
Adobe Systems, Inc.	Scott Foshee Steve Zilles (Alt.)
AIM Global, Inc.	Dan Mullen Charles Biss (Alt.)
Apple Computer, Inc.	Kwok Lau Helene Workman (Alt.) David Singer (Alt.)
Distributed Management Task Force	John Crandall Jeff Hilland (Alt.)
Electronic Industries Alliance	Edward Mikoski, Jr. Henry Cuschieri (Alt.)
EMC Corporation..... Farance, Inc.	Gary Robinson Frank Farance Timothy Schoechle (Alt.)
Google..... GS1 US	Zaheda Bhorat Ray Delnicki Frank Sharkey (Alt.) James Chronowski (Alt.) Mary Wilson (Alt.)
Hewlett-Packard Company.....	Karen Higginbottom Paul Jeran (Alt.)
IBM Corporation	Gerald Lane Robert Weir (Alt.)
IEEE	Bill Ash Terry DeCourcelle (Alt.) Jodie Haasz (Alt.) Bob Labelle (Alt.)

<i>Organization Represented</i>	<i>Name of Representative</i>
Intel	Philip Wennblom Grace Wei (Alt.) Steven Balogh (Alt.)
Lexmark International.....	Don Wright Dwight Lewis (Alt.) Paul Menard (Alt.)
Microsoft Corporation	Jim Hughes Dave Welsh (Alt.) Mark Ryland (Alt.) John Calhoun (Alt.)
National Institute of Standards & Technology	Michael Hogan Elaine Barker (Alt.) Dan Benigni (Alt.) Fernando Podio (Alt.) Teresa Schwarzhoff (Alt.) Wo Chang (Alt.)
Oracle Corporation	Donald R. Deutsch Jim Melton (Alt.)] Michael Kavanaugh (Alt.) Toshihiro Suzuki (Alt.) Jeff Mischkinsky (Alt.) Tony DiCenzo (Alt.) Eduardo Gutentag (Alt.)
Purdue University	Stephen Elliott
Storage Networking Industry Association (SNIA).....	Gary Phillips Arnold Jones (Alt.) Dave Thiel (Alt.)
US Department of Defense	Jerry Smith Dennis Devera (Alt.) Dave Brown (Alt.) Leonard Levine (Alt.)
US Department of Homeland Security	Peter Shebell Gregg Piermarini (Alt.)

Technical Committee T10 on SCSI Storage Interfaces, which developed and reviewed this standard, had the following members:

John B. Lohmeyer, Chair
Mark Evans, Vice-Chair
Ralph O. Weber, Secretary

<i>Organization Represented</i>	<i>Name of Representative</i>
Amphenol Interconnect	Gregory McSorley Adrian Green (Alt.) Alex Persaud (Alt.) Michael Wingard (Alt.)
Applied Micro Circuits Corporation.....	Scott Furey
Brocade.....	David Peterson Scott Kipp (Alt.)
Dell, Inc.	Kevin Marks
EMC Corporation	Gary Robinson David Black (Alt.) Adrianus Djohan (Alt.) Sean Dolan (Alt.) Mickey Felton (Alt.) Robert Payne (Alt.)
Emulex	William Martin Robert Nixon (Alt.)
ENDL Texas	Ralph Weber I Dal Allan (Alt.)
FCI Electronics	Douglas Wagner

<i>Organization Represented</i>	<i>Name of Representative</i>
Finisar Corporation	David Freeman Chris Cicchetti (Alt.) Dominic Coupal (Alt.) Paul Gentieu (Alt.) Geoff Hibbert (Alt.) Monica Li (Alt.) Katharine Schmidtke (Alt.)
Foxconn Electronics	Elwood Parsons
Fujitsu Comp Prod of America.....	Mike Fitzpatrick Don Jeanette (Alt.) Osamu Kimura (Alt.)
General Dynamics	Nathan Hastad Tim Mackley (Alt.)
Hewlett-Packard Company.....	Rob Elliott Curtis Ballard (Alt.) Michael Banther (Alt.) Wayne Bellamy (Alt.) Steven Fairchild (Alt.) Barry Olawsky (Alt.) Christopher Williams (Alt.) Jeff Wolford (Alt.)
Hitachi Global Storage Technologies	Dan Colegrove Dan Reno (Alt.)
IBM Corporation	Kevin Butt Mark Andresen (Alt.) Ted Vojnovich (Alt.)
Intel Corporation	Mark Seidel Heath Seabourn (Alt.) Pak-Lung Seto (Alt.) Sarah Sharp (Alt.)
Kawasaki Microelectronics America Inc	Joel Silverman
KnowledgeTek Inc	Dennis Moore Hugh Curley (Alt.)
Lexar Media Inc.....	John Geldman Neal Galbo (Alt.)
LSI Corporation	John Lohmeyer Brad Besmer (Alt.) Brian Day (Alt.) Keith Holt (Alt.) Walt Hubis (Alt.) Steve Johnson (Alt.) Bernhard Laschinsky (Alt.) Harvey Newman (Alt.) George Penokie (Alt.) Robert Sheffield (Alt.)
Marvell Semiconductor Inc	David Geddes Jacky Chow (Alt.) Paul Wassenberg (Alt.)
Maxim Integrated Products.....	Gregory Tabor David Allen (Alt.) Mahbubul Bari (Alt.) Samuel Barnett (Alt.) Kevin Witt (Alt.)
Microsoft Corporation	Calvin Chen Mark Benedict (Alt.) Robert Griswold (Alt.) Frank Shu (Alt.)
Molex Inc	Jay Neer Galen Fromm (Alt.)
NetApp.....	Frederick Knight Chris Fore (Alt.) Subhash Sankuratripati (Alt.)
NVidia Corporation	Mark Overby Andrew Currid (Alt.)

<i>Organization Represented</i>	<i>Name of Representative</i>
PMC-Sierra	Tim Symons Guillaume Fortin (Alt.) Mathieu Gagnon (Alt.) Rick Hernandez (Alt.) Neil Wanamaker (Alt.)
Quantum Corporation.....	Paul Suhler Paul Stone (Alt.) Rod Wideman (Alt.)
Quellan.....	Gourgen Oganessyan
Samsung Information Systems	Joseph Chen Edward Chang (Alt.) Sung Lee (Alt.) Dmitry Obukhov (Alt.) John Osterlund (Alt.)
SanDisk IL Ltd.	Avraham Shimor Dave Landsman (Alt.) Yoni Shternhell (Alt.)
Seagate Technology	Gerald Houlder Alvin Cox (Alt.) Jim Hatfield (Alt.) Judy Westby (Alt.)
Sun Microsystems Inc	Dale LaFollette Jon Allen (Alt.) Dennis Appleyard (Alt.) Matthew Ball (Alt.) Vit Novak (Alt.) Scott Painter (Alt.) Roger Cummings Raymond Gilson (Alt.)
Symantec	Scott Shuey Michael Fogg (Alt.) Dan Gorenc (Alt.) Tom Grzysiewicz (Alt.) John Hackman (Alt.) Andy Nowak (Alt.) Robert Wertz (Alt.)
Tyco Electronics.....	Atul Sharma Toney Chew (Alt.) Mark Evans Michael Rogers (Alt.) Curtis Stevens (Alt.)
Volex Inc	
Western Digital Corporation	

Introduction

The SCSI / ATA Translation - 2 (SAT-2) standard is divided into the following clauses:

- Clause 1 defines the scope of this standard.
- Clause 2 enumerates the normative references that apply to this standard.
- Clause 3 describes the definitions, symbols, abbreviations, and notation conventions used in this standard.
- Clause 4 describes the general framework for defining elements of translation between SCSI and ATA protocol.
- Clause 5 describes elements of SCSI / ATA Translation that relate to the SCSI architecture model.
- Clause 6 describes the mapping of command management functions in the SATL layer.
- Clause 7 provides a summary of SCSI commands mapped to ATA in this standard.
- Clause 8 describes the mapping between SCSI Primary Commands and ATA protocol.
- Clause 9 describes the mapping between SCSI Block Commands and ATA protocol.
- Clause 10 describes the mapping of mode pages, log pages, and VPD page information to selected ATA protocol elements.
- Clause 11 describes error reporting and sense data conventions for SCSI / ATA Translation.
- Clause 12 describes SCSI commands and mode pages to support SCSI / ATA Translation.
- Annex A describes command translation for ATAPI devices.

American National Standard
for Information Technology—

SCSI/ATA Translation - 2 (SAT-2)

1 Scope

The set of SCSI standards specifies the interfaces, functions, and operations necessary to ensure interoperability between conforming SCSI implementations. This standard is a functional description. Conforming implementations may employ any design technique that does not violate interoperability.

This standard defines the protocol requirements of the SCSI / ATA Translation Layer (SATL) to allow conforming SCSI / ATA translating components to interoperate with ATA devices and SCSI application layers. The SATL covers the range of implementations that use ATA devices to emulate the behavior of SCSI devices as viewed by the SCSI application layer. The primary focus of this standard is to define SCSI / ATA Translation for an ATA device (see 3.1.9).

Where possible, this standard defines SCSI / ATA Translation in a manner that is consistent with the SAM-4, SPC-4, and SBC-3 standards. In some instances, the defined function of an ATA device is different from corresponding functions defined for SCSI target devices (e.g., an ATA device provides no means to abort a single ATA queued command). The translation defined in this standard, in such cases, may not be consistent with other SCSI standards. However, in such cases, this standard specifies the expected behavior, and in what manner it is inconsistent with the behavior specified in other SCSI standards.

The objective of this standard is to allow a complete set of SCSI functions while minimizing the complexity of the SATL and preserving compatibility with existing SCSI application clients.

The objectives of the SATL are:

- a) to provide host computers with device independence with respect to the ATA devices that have user storage capacity, and with respect to various implementations of the translation layer used to emulate the behavior of SCSI target devices;
- b) to define common features and functions representing a subset of the capabilities available in SCSI devices that apply to SCSI / ATA Translation implementations;
- c) to define common methods to manage aspects of ATA devices that do not map to previously defined features and functions of SCSI, with provision made for the addition of special features and functions; and
- d) to provide consistent means for discovery and control of optional SCSI features that may or may not be emulated in SCSI / ATA translator implementations. These means are provided by specifying how transport specific features and functions are represented in a mixed-domain topology in a manner consistent with management of devices in a SCSI domain.