



IPC-6017

Qualification and Performance Specification for Printed Boards Containing Embedded Passive Devices

Developed by the Embedded Devices Performance Subcommittee (D-53) of the Embedded Devices Committee (D-50) of IPC

Users of this publication are encouraged to participate in the development of future revisions.

Contact:

IPC
3000 Lakeside Drive, Suite 309S
Bannockburn, Illinois
60015-1249
Tel 847 615.7100
Fax 847 615.7105

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1 SCOPE

1.1 Statement of Scope This specification covers qualification and performance of in-process and finished printed boards (PBs) containing embedded passive circuitry with distributive capacitive planes, capacitive or resistive components.

1.2 Purpose The requirements contained herein are intended to reflect electrical, mechanical, and environmental properties unique to embedded passives. It is not intended to specify overall requirements for the PB which are already documented in the following sectional performance specifications: IPC-6012 (rigid), IPC-6013 (flex/rigid-flex), IPC-6015 (MCM-L) or IPC-6018 (microwave).

1.3 Performance Classification This specification recognizes that in-process and finished PBs containing embedded passive devices will be subject to variations in performance requirements based on end-use. The PBs are classified by one of three general Performance Classes as defined in IPC-6011.

1.4 Embedded Passive Device Categories Embedded resistors and capacitors are supplied in two formats, which are referred to as laminate and nonlaminar like. The laminar like are supplied as sheets of capacitor or resistor materials usually supplied with copper foil and/or an inner-layer support. The final resistors or capacitors designs are imaged/etched in the supplied material. Nonlaminar like capacitors and resistors are supplied as pastes or other materials which are applied in discrete areas to define the capacitors and resistors. Both categories of embedded passives are included in this specification.

1.5 Documentation Hierarchy This document, combined with IPC-6011 and the applicable sectional performance specification(s) (e.g., IPC-6012, IPC-6013, IPC-6018, etc.), constitutes a qualification and performance specification for in-process and finished PBs containing embedded passives.

1.6 Selection for Procurement For procurement purposes, performance class **shall** be specified in the procurement documentation. The documentation **shall** provide sufficient information to the supplier so that one can fabricate

the PB and ensure that the user receives the desired product. Information that should be included in the procurement documentation is shown in IPC-D-325.

1.7 Selection (Default) The procurement documentation should specify the requirements that can be selected within this specification; however, in the event selections are not made in the applicable sectional performance specification(s) (e.g., IPC-6012, IPC-6013, IPC-6018, etc.), then the Class 2 requirements in those specifications **shall** apply.

1.8 Terms and Definitions Terms and definitions **shall** be in accordance with IPC-T-50 and as stated below.

1.8.1 As Agreed Upon Between User and Supplier (AABUS) Indicates additional or alternate requirements to be decided between the user and the supplier in the procurement documentation. Examples include contractual requirements, modifications to purchase documentation and information on the drawing. Agreements can be used to define test methods, conditions, frequencies, categories or acceptance criteria within a test, if not already established.

Note: The user's attention is called to the possibility that utilization of some of the materials described within this design guideline may require use of inventions covered by rights of patents. By publication of this document, no position is taken with respect to the validity of these claims or of any rights of patents in connection therewith. The patent holders have, however, filed a statement of willingness to grant a license under the rights of these patents on reasonable and nondiscriminatory terms and conditions to applicants desiring to obtain such a license. Details may be obtained from IPC.

2 APPLICABLE DOCUMENTS

2.1 IPC¹

IPC-T-50 Terms and Definitions for Interconnecting and Packaging Electronic Circuits

IPC-D-325 Documentation Requirements for Printed Boards, Assemblies, and Support Drawings

IPC-TM-650 Test Methods Manual²

2.1.1 Microsectioning

1. www.ipc.org

2. Current and revised IPC Test Methods are available on the IPC Web site (www.ipc.org/html/testmethods.htm)