

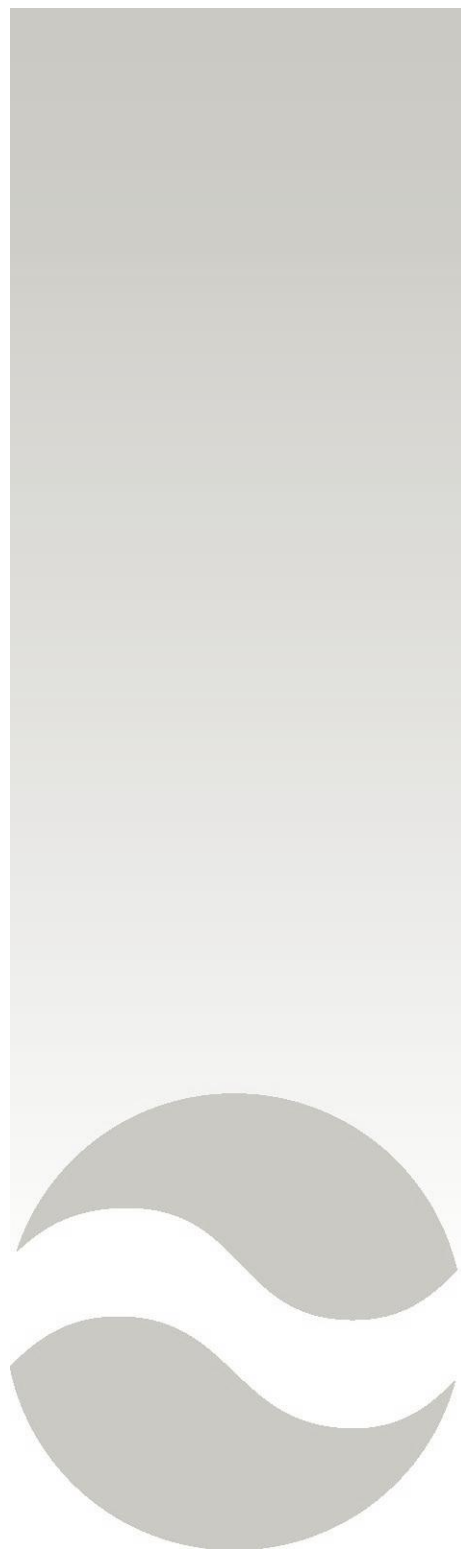
**IPC/JEDEC J-STD-609C**  
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# ***JOINT INDUSTRY STANDARD***

Marking, Symbols, and  
Labels of Leaded and  
Lead-Free Terminal  
Finished Materials Used in  
Electronic Assembly

*Association Connecting Electronics Industries*



**MARKING, SYMBOLS, AND LABELS OF LEADED AND LEAD-FREE TERMINAL FINISHED MATERIALS USED IN ELECTRONIC ASSEMBLY**

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## **MARKING, SYMBOLS, AND LABELS OF LEADED AND LEAD-FREE TERMINAL FINISHED MATERIALS USED IN ELECTRONIC ASSEMBLY**

(From JEDEC Board Ballot JCB-23-45, formulated under the cognizance of the JC-14.4 Subcommittee on Quality Processes and Methods.)

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### **1 Scope**

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This standard applies to components and assemblies that contain Pb-free and Pb-containing solders and finishes. This standard describes the marking of components and the labeling of their shipping containers to identify their 2<sup>nd</sup> level terminal finish or material, and applies to components that are intended to be attached to boards or assemblies with solder or mechanical clamping or are press fit. This standard also applies to 2<sup>nd</sup> level terminal materials for bumped die that are used for direct board attach.

This standard applies to boards/assemblies, to identify the type of Pb-free or Pb-containing solder used. This standard documents a method for identifying board surface finishes and Printed Circuit Board (PCB) resin systems. This standard applies to PCB base materials and for marking the type of conformal coating utilized on Printed Circuit Board Assemblies (PCBAs). Material and their containers previously marked or labeled according to JESD 97, IPC-1066, or previous versions of this standard need not be remarked unless agreed upon by the supplier and customer.

Labeling of exterior surfaces of finished articles, such as computers, printers, servers, and the like, is outside the scope of this standard. However internal PCBs and PCBAs are covered by this standard. Labeling of retail packages containing electronic products is also outside the scope of this standard. Markings under this standard do not denote EU RoHS compliance, or any other regional substance restriction legislation addressing lead content.

#### **1.1 Purpose**

This standard provides a marking and labeling system that aids in assembly, rework, repair and recycling and provides for the identification of:

- (1) those assemblies that are assembled with Pb-containing or Pb-free solder;
- (2) components that have Pb-containing or Pb-free 2<sup>nd</sup> level interconnect terminal finishes and materials;
- (3) the maximum component temperature not to be exceeded during assembly or rework processing;
- (4) the base materials used in the PCB construction, including those PCBs that use halogen-free resin;
- (5) the surface finish of PCBs; and
- (6) the conformal coating on PCBAs.