IPC/JPCA-2291

Design Guideline for Printed Electronics

Developed by the Printed Electronics Design Subcommittee (D-61) of the Printed Electronics Committee (D-60) of IPC

Users of this publication are encouraged to participate in the development of future revisions.

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DESIGN GUIDELINES FOR PRINTED ELECTRONICS

1 SCOPE

This guideline provides an overview of the design process flow for printed electronics based devices, modules and units, and final products.

1.1 Purpose The purpose is to present the framework of the design process flow for individuals to manufacture printed electronics based devices, modules and units, and final products.

1.2 Intent The intent of IPC/JPCA-2291 is to establish a design process flow that will facilitate and improve the practice of printed electronics design. IPC/JPCA-2291 identifies documents such as standards that can be used to assist during the design process flow. In general IPC/JPCA-2291 contains generic information that is sufficient for printed electronics design by the product designer.

The subcommittee members that developed IPC/JPCA-2291 acknowledge that individual companies may require additional information than that reported within this document. Therefore, IPC/JPCA-2291 and documents specified within it may only reflect a subset of those required by members of the supply chain.

IPC/JPCA-2291 is ‘‘generic’’ because it specifies only information which forms the basis for further specific declarations. It is therefore intended to be used in conjunction with other documents as needed. Also, part of the intent is to provide mechanisms for securing the integrity of the information exchanged between supply chain members.

1.3 Printed Electronics Document Hierarchy The IPC/JPCA printed electronics standards development subcommittees have established a hierarchy as presented in Figure 1-1 based on the existing initiatives. It was structured to enable the greatest flexibility for the emerging field of printed electronics. The subcommittees plan to revisit it frequently and modify it as necessary during the growth of the field and technologies transition from R&D to commercialization.

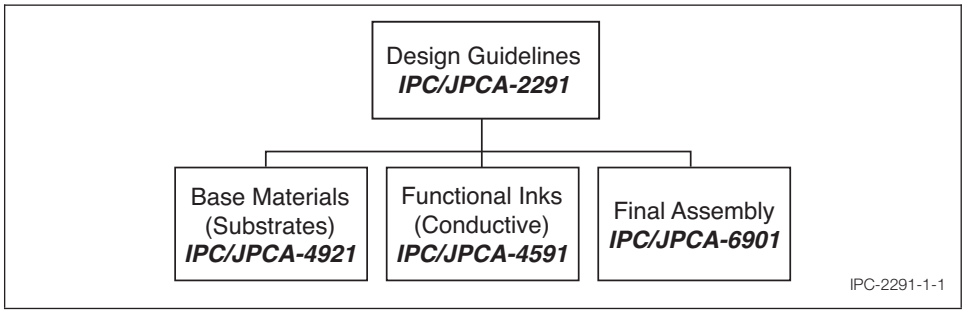

Figure 1-1 Hierarchy for IPC/JPCA Printed Electronics Documents

As the documents listed in Figure 1-1 are completed, new standards projects may be undertaken by the subcommittees. Proposed projects will be listed as shown in Figure 1-2; a project was proposed that addresses manufacturing processes and platforms. In addition to initiating new project topics, subcommittees may also be actively preparing a revision of a previously approved standard based on recent subcommittee member comments as well as general industry trends.

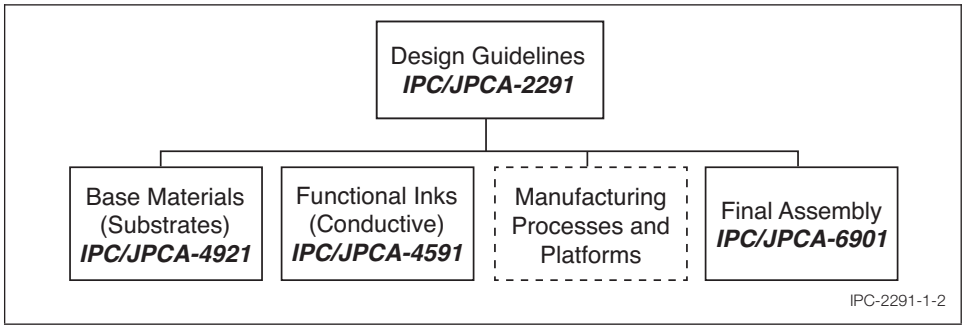

Figure 1-2 Future Hierarchy for IPC/JPCA Printed Electronics Documents