STANDARD **11518-1**

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Information technology — High-Performance Parallel Interface —

Part 1:
Mechanical, electrical and signalling protocol specification (HIPPI-PH)

Technologies de l'information — Interface parallèle à haute performance —

Partie 1: Spécification du protocole mécanique, électrique et de signalisation (HIPPI-PH)

Reference number
ISO/IEC 11518-1:1995(E)

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Foreword

ISO (the International Organization for Standardization) and IEC (the International Electrotechnical Commission) form the specialized system for worldwide standardization. National bodies that are members of ISO or IEC participate in the development of International Standards through technical committees established by the respective organization to deal with particular fields of technical activity. ISO and IEC technical committees collaborate in fields of mutual interest. Other international organizations, governmental and non-governmental, in liaison with ISO and IEC, also take part in the work.

In the field of information technology, ISO and IEC have established a joint technical committee, ISO/IEC JTC 1. Draft International Standards adopted by the joint technical committee are circulated to national bodies for voting. Publication as an International Standard requires approval by at least 75 % of the national bodies casting a vote.

International Standard ISO/IEC 11518-1 was prepared by Joint Technical Committee ISO/IEC JTC 1, *Information technology*, Subcommittee SC 25, *Interconnection of information technology equipment*.

ISO/IEC 11518 consists of the following parts, under the general title *Information technology — High-Performance Parallel Interface*:

— *Part 1: Mechanical, electrical and signalling protocol specification (HIPPI-PH)*

— *Part 2: Framing Protocol (HIPPI-FP)*

— *Part 3: Encapsulation of ISO 8802-3 — Logical link control protocol data units (HIPPI-LE)*

— *Part 4: Mapping of HIPPI to IPI device generic command sets (HIPPI-IPI)*

— *Part 5: Memory Interface (HIPPI-MI)*

— *Part 6: Physical Switch Control (HIPPI-SC)*

Annexes A to E of this part of ISO/IEC 11518 are for information only.

Introduction

This part of ISO/IEC 11518 defines the physical layer of an efficient simplex high-performance point-to-point interface operating at speeds of 800 or 1 600 Mbit/s. The -PH abbreviation stands for "physical layer".

Characteristics of this HIPPI physical layer interface include

– point-to-point connections use one or two copper twisted-pair cables for distances of up to 25 m.

– the HIPPI-PH is a simplex interface, capable of transferring data in one direction only. Two HIPPI-PHs may be used to implement a full-duplex interface.

– data transfers are performed and flow controlled in increments of bursts, each burst normally containing 256 words.

– signalling and control sequences are kept simple, and a look-ahead flow control is used, to allow average transfer rates for large file transfers to approach the peak transfer rate, even over distances longer than specified for the HIPPI-PH cables.

– the HIPPI-PH provides support for low-latency, real-time, and variable size packet transfers.

– the HIPPI-PH is designed to facilitate use in a circuit-switched environment. In support of this feature, a limited information field is available for subdevice addressing or other nonspecified control functions during the connection phase of operation. One round-trip cable delay is required to establish or terminate a connection.

– the HIPPI-PH is also designed to transmit multiple packets after a connection has been established. No round-trip cable delays are required between packets.

Figure 1 shows the interrelationship of the different clauses of this part of ISO/IEC 11518. The upper-layer protocols and station management protocols are not covered in this part of ISO/IEC 11518.

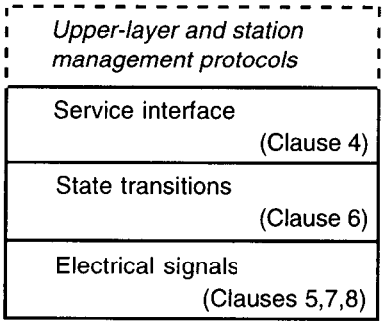

Figure 1 – Control hierarchy